

YOR919980423US2



SUBSTITUTE
SPECIFICATION &
ABSTRACT

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ADDRESS WRAP FUNCTION FOR ADDRESSABLE MEMORY DEVICES

{0001} This Application is a Continuation in Part Application of parent Application Serial Number

09/419,514 Filed 10/18/99, now abandoned, which claims benefits of 69/104,889, filed 10/20/1998.

{0002} Cross reference is made to Application Serial No. (IBM Docket YOR919980423US3) which is a contemporaneously filed Continuation in Part Application of the above identified parent of this application.

Field of the invention

{0003} The invention relates to the providing of a wrap function useable in connection with an addressable random access memory that will wrap the address portion of data for additional use without disturbing the contents of the memory element and in particular to a special command or function capability that will selectably bypass the storage part of the memory assembly and pass the address portion on for use in collateral applications.

Background

{0004} In many data processing operations the information being processed is in the form of increments or words that carry an accumulation portion that is to be stored and an identification portion that tells the memory array where to put it. As progress in the art has taken place the arrays have become huge, the housekeeping and control circuitry very complex and the stored information both large and valuable. There have long been problems with such arrays in the ever increasing speed and size environment with testing, with timing and with component drift as examples. Where the memory array is made up of

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